

## Data Sheet

## ADL8106

### ABSOLUTE MAXIMUM RATINGS

**Table 6.**

Parameter	Rating
Drain Bias Voltage ( $V_{DD1}$ and $V_{DD2}$ )	4 V
Negative Gate Bias Voltage ( $V_{GG1}$ )	-2.1 V to 0 V
RF Input Power (RFIN)	17 dBm
Continuous Power Dissipation ( $P_{DISS}$ ), $T_{CASE} = 85^{\circ}\text{C}$ (Derate 16.6 mW/ $^{\circ}\text{C}$ Above 85 $^{\circ}\text{C}$ )	1.49 W
Temperature	
Storage Range	<b>-55<math>^{\circ}\text{C}</math> to +150<math>^{\circ}\text{C}</math></b>
Operating Range	-40 $^{\circ}\text{C}$ to +85 $^{\circ}\text{C}$
Quiescent Channel ( $T_{CASE} = 85^{\circ}\text{C}$ , $V_{DDx} = 3\text{ V}$ , $I_{DQ} = 120\text{ mA}$ , Input Power ( $P_{IN}$ ) = Off)	106.7 $^{\circ}\text{C}$
Maximum Channel	175 $^{\circ}\text{C}$

Stresses at or above those listed under Absolute Maximum Ratings may cause permanent damage to the product. This is a stress rating only; functional operation of the product at these or any other conditions above those indicated in the operational section of this specification is not implied. Operation beyond the maximum operating conditions for extended periods may affect product reliability.

### THERMAL RESISTANCE

Thermal performance is directly linked to system design and operating environment. Careful attention to the PCB thermal design is

### ELECTROSTATIC DISCHARGE (ESD) RATINGS

The following ESD information is provided for handling of EDS-sensitive devices in an ESD protected area only.

Human body model (HBM) per ANSI/ESDA/JEDEC JS-001.

### ESD Ratings for ADL8106

**Table 8. ADL8106, 24-Terminal LGA\_CAV**

ESD Model	Withstand Threshold (V)	Class
HBM	±300	1A

### ESD CAUTION



**ESD (electrostatic discharge) sensitive device.** Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.